DR. BABASAHEB AMBEDKAR RESEARCH & TRAINING INSTITUTE (BARTI), PUNE

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Outward No./Barti Pune/Skill Development-03/2024-25/ ২৫৪২   Date: 29|9|24

ADMISSION for PG Diploma Courses in HPC domain at C-DAC: Centre for Development of Advanced Computing

C-DAC is implementing the National Supercomputing Mission (NSM nsmindia.in), a capacity building initiative of Ministry of Electronics and Information Technology (MeitY), Government of India. The objective of the project is to develop technology professionals hailing from Scheduled Castes (SC) communities, having graduation in Engineering or post-graduation in science disciplines, in High Performance Computing (HPC) domains to meet skill and capability building objectives of the NSM.

There are two domains identified to develop the expertise which are:

1. High Performance Computing System Administration (PG-DHPCSA)
2. High Performance Computing Application Programming (PG-DHPCAP)

Eligibility Criteria:

1. Candidate must belong to Scheduled Caste in India.
2. Candidate must be Graduate in Engineering (10+2+4 or 10+3+3 years) in IT / Computer Science / Electronics / Telecommunications / Electrical / Instrumentation, OR MSc / MS (10+2+3+2 years) in Computer Science, IT, Electronics, OR Post Graduate Degree in Mathematics / allied areas, OR MCA from a recognized University.
3. For PG-DHPCAP course candidates having Post Graduate Degree in Physics / Computational Sciences are also eligible.
4. Candidate must have secured 55% or above in the qualifying degree.
5. Candidate who has appeared for their final examination of the qualifying degree in 2024 may also apply subject to submitting their proof of degree certificate to C-DAC by the date prescribed for regular C-CAT admission (December 31, 2024).

Eligibility: SC candidates meeting Educational Qualification under this program.
Intake Capacity: 40 seats per course.
Course Duration: 6 Months (900 hrs)
Fee: Free for admitted candidates [The NSM PG-Diploma courses in HPC domain are free for SC]
Stipend: A stipend at the rate of INR 1500/- per month, will be provided to the student for six months. The stipend will be provided in two instalments at the end of each three months periods subject to student maintaining 75% attendance for that period. The stipend will be deposited in the account of the student.
For More details: rb.gy/8mywgy
Registration Form for NSM C-CAT: rb.gy/81cd7o

Mr.Sunil Ware (IRAS)
Director General
Dr. Babasaheb Ambedkar Research and
Training Institute (BARTI), Pune

Admission for Post Graduate Diploma in
HPC Domain for Socially Disadvantage Group
(SC and ST communities of India)

NSM C-CAT registration & application link
https://www.cdac.in/NSMTrainingSCST

Sponsored from Ministry of Electronics and Information Technology (MeitY), Government of India under National Supercomputing Mission

Centre for Development of Advanced Computing (C-DAC) is sponsoring candidates from the Scheduled Castes (SC) and Scheduled Tribes (ST) communities' candidates to apply for the Post Graduate Diploma Courses in High Performance Computing (HPC) domain under NSM capacity building programme.

The objective of NSM capacity building programme is to provide career opportunities in the ICT industry especially the High Performance Computing (HPC) and Super Computing targeting the System Administration, Application programming, and Artificial Intelligence Development domains.

Post Graduate Diploma in HPC domain

1. Post Graduate Diploma in HPC System Administration (PG-DHPCSA)
2. Post Graduate Diploma in HPC Application Programming (PG-DHPCAP)

Important Dates

- Start Date of Online Registration and Application : May 28, 2024
- Last Date of Online Registration : June 26, 2024
- NSM C-DAC's Common Admission Test (C-CAT) : July 06 - 07, 2024
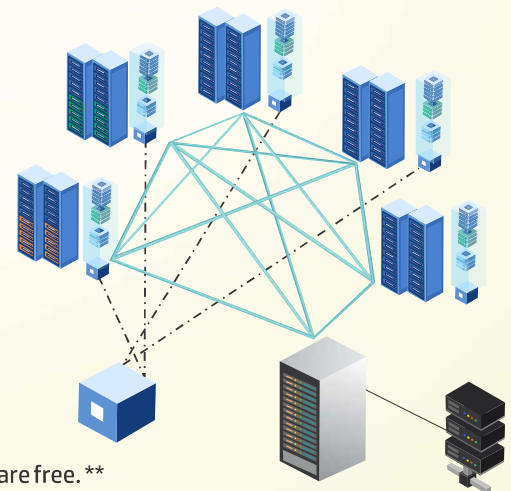- Commencement of Course : August 29, 2024

Benefits

1. Sponsorship for the NSM C-CAT exam and the PG Diploma courses in HPC domains are free. **
2. Nominal Stipend will be provided to the candidates during the PG Diploma training.

** Conditions apply (visit website and refer the Rule of PG Diploma in HPC domain for more details)

Eligibility

- Candidate must belong to Scheduled Caste and Scheduled Tribes communities

- Graduate in Engineering (10+2+4 or 10+3+3 years) Electronics/ Computer Science/ IT /Telecommunications / Electrical / Instrumentation OR MSc/MS (10+2+3+2 years) in Computer Science, IT, Electronics OR MCA OR Post Graduate in Physics/ Computational Sciences / Mathematics or allied areas. The candidates must have secured a minimum of 55% marks in their qualifying examination.

- Candidate who have appeared for their final examination of the qualifying degree in 2024 may also apply subject to submitting their proof of degree certificate to C-DAC by the date prescribed for regular C-CAT admission (Dec 31, 2024).

- A candidate can avail PG Diploma course benefit only once.